

integration with integrity

User's Manual Single Board Computer 3308550 Version 1.1, September 2012

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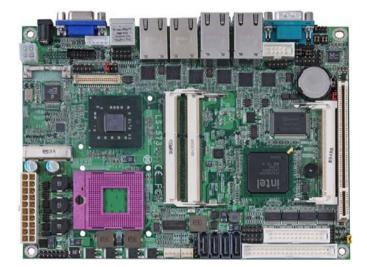
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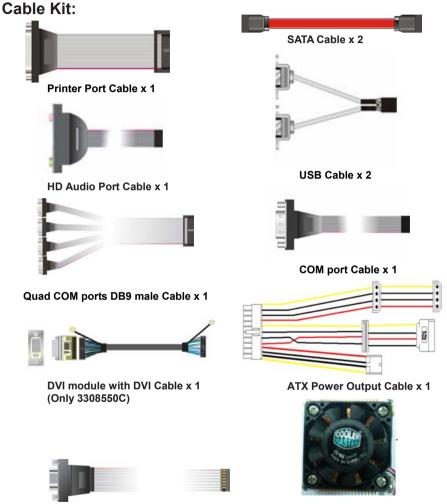


Packing List:

Please check the package content before you starting using the board.

Hardware:

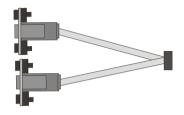
3308550 Embedded Miniboard x 1



CRT cable x 1 (Only 3308550B)

CPU Cooler x 1





HDTV cable x 1 (Optional)

SDTV cable x 1 (Optional)

Printed Matters:

Driver CD x 1 (Including User's Manual)

Chapter 1 < Introduction>

1.1 <Product Overview>

3308550, a new generation of the 5.25" Miniboard, supports Intel Penryn Processor for 667/800/1066 MHz front side bus and features Intel GM45 and ICH9M chipset, integrated GMA X4500 graphics, DDR3 memory, REALTEK ALC888 High Definition Audio, Serial ATA and six Intel Gigabit LAN.

Intel Penryn Processor

The board supports Intel Penryn processor with 667/800/1066 MHz front side bus, L2 Cache: All specification depend on the CPU. (1M/2M/3M/4M/6M). To provide more powerful performance than before.

New features for Intel GM45 chipset

The board integrates Intel GM45 and ICH9M chipset, to provide new generation of the mobile solution, supports Intel GMA X4500 graphics, DDR3 800/1066MHz memory, built-in high speed mass storage interface of serial ATA, High Definition Audio with 2 channels surrounding sound.

All in One multimedia solution

Based on Intel GM45 and ICH9M chipset, the board provides high performance onboard graphics, 24-bit dual channel LVDS interface, HDTV and 2 channels High Definition Audio, to meet the very requirement of the multimedia application.

Flexible Extension Interface

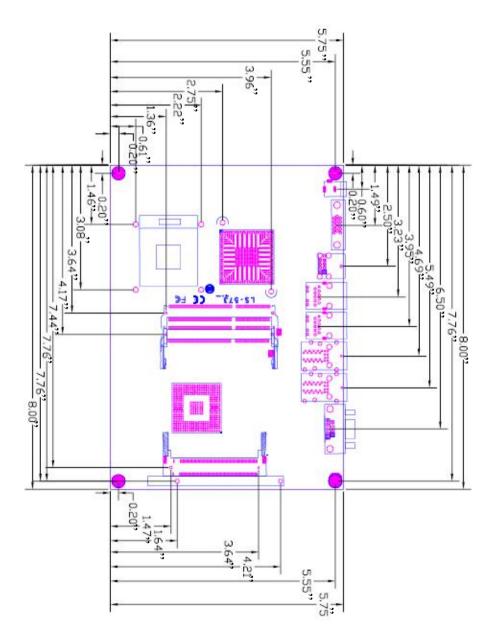
The board provides one mini PCI Express, one mini-PCI socket and one PCI slot.

1.2 <Product Specification>

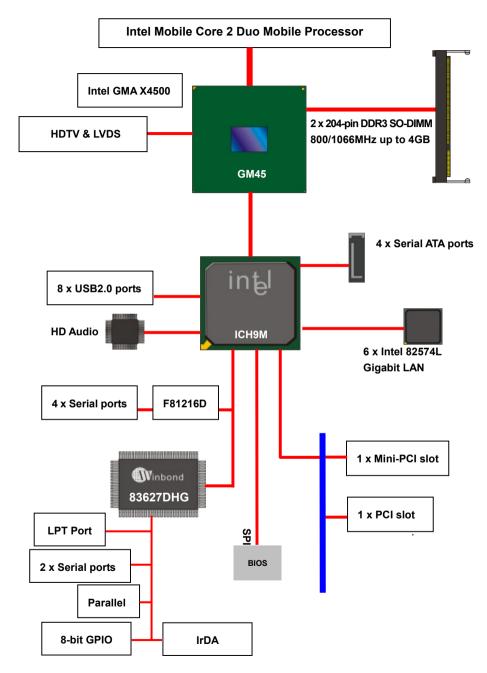
General Specifica	tion		
Form Factor	5.25 Inches Embedded Miniboard		
CPU	Support Intel Penryn Processor		
	Package type: Micro-FCPGA478 (Socket-P)		
	L2 Cache: All specification depend on the CPU. (1M/2M/3M/4M/6M)		
	Front side bus: 667/800/1066 MHz		
Memory	2 x 800/1066MHz DDRIII 204-pin SO-DIMM up to 4GB		
Chipset	Intel® GM45 and ICH9M (82801IBM)		
BIOS	Phoenix-Award v6.00PG 8Mb SPI flash BIOS		
Green Function	Power saving mode includes doze, standby and suspend modes.		
	ACPI version 1.0 and APM version 1.2 compliant		
Watchdog Timer	System reset programmable watchdog timer with 1 ~ 255 sec./min.		
	of timeout value		
Real Time Clock	Chipset integrated RTC with onboard lithium battery		
Serial ATA	Intel® ICH9M built-in 4 x Serial ATAII interface up to 300MB/s		
Multi-I/O Port			
Chipset	Intel® ICH9M with Winbond® W83627DHG controller		
Serial Port	Five RS232 and one jumper selectable RS232/422/485/IR		
USB Port	8 x Hi-Speed USB 2.0 ports with 480Mbps of transfer rate		
IrDA Port	One IrDA compliant Infrared interface supports SIR		
K/B & Mouse	PS/2 keyboard and mouse port on bracket		
GPIO	One 12-pin Digital I/O connector with 8-bit programmable		
Smart Fan	One CPU fan connectors for fan speed controllable		
VGA Display Interfa	ce		
Chipset	Intel® GM45 & ICH9M		
Memory	Up to 384MB shared with system memory		
Display Type	CRT, LCD monitor with analog display, DVI or LVDS		
Connector	External DB15 female connector		
	Onboard 40-Pin LVDS connector		
	Onboard 10-Pin DB15 connector(3308550B Only)		
	Onboard 26-Pin DVI connector (3308550C Only)		
Ethernet Interface			
Chipset	Intel 82573L Gigabit Ethernet controller		
Туре	Triple speed 10/100/1000Base-T		
	auto-switching Fast Ethernet		
	Full duplex, IEEE802.3U compliant		
Connector	External six RJ45 connector with LED		

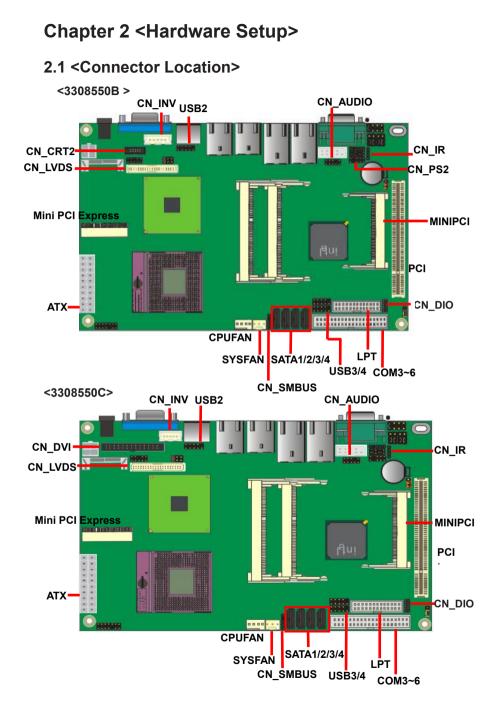
Audio Interface			
Chipset	Intel® ICH9M with Realtek ALC888 HD Audio		
	Intel High Definition Audio compliance		
Interface	2 channels sound output		
Connector	Internal 10-pin header for line-in/-out, MIC-in, 4-pin header for CD-I		
ower and Enviro	nment		
Power	Standard 20-Pin ATX power supply or 9V~24V DC Input		
Requirement			
Dimension	203 (L) x 146 (W) mm		
Temperature	Operating within 0 ~ 60° C (32 ~ 140° F)		
	Storage within -20 ~ 85°C (-4 ~ 185°F)		
Ordering Code			
3308550A	Onboard CRT, LVDS, HDTV, 6 x Intel Gigabit LAN, 8 x USB2.0, PCI, Mini-PC		
	GPIO, IrDA, PCI Express mini card, Parallel, RS232/422/485, 4 x SATA, HD		
	Audio and SMBus, PCI		
3308550B	Onboard 2 X CRT, LVDS, HDTV, 6 x Intel Gigabit LAN, 8 x USB2.0, PCI,		
	Mini-PCI, GPIO, IrDA, PCI Express mini card, Parallel Port, RS232/422/485,		
	SATA, HD Audio and SMBus, PCI		
3308550C	Onboard CRT, HDTV, 6 x Intel Gigabit LAN, 8 x USB2.0, PCI, Mini-PCI, GPI		
	IrDA, PCI Express mini card, Parallel, RS232/422/485, 4 x SATA, HD Audio a		
	SMBus, DVI, PCI		

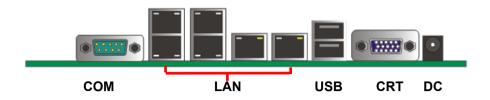
1.3 < Mechanical Drawing>



1.4 <Block Diagram>

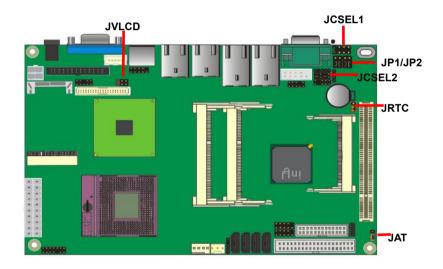






2.2 <Jumper Location & Reference>

Jumper	Function
JRTC	CMOS Operating/Clear Setting
JVLCD	Panel Voltage Setting
JAT	Power mode select
JP1	COM1 signal mode switch (For Pin-1 & Pin-9)
JP2	COM2 signal mode switch (For Pin-1 & Pin-9)
JCSEL1	CN_COM2 RS-232 RS422 RS485 Setting / CN_IR IrDA
JCSEL2	Setting



Jumper: **JAT**

Type: onboard 3-pin header

Power Mode	JAT
AT Mode	1-2
ATX Mode	2-3
Default setting: 2-3	1 3

Jumper: JP1 (COM 1)

Type: onboard 3 x 2-pin header

Power Mode	JP1			
Standard COM Port	3-5,4-6			
Pin1 with 5V signal	1-3,4-6			
Pin9 with 12V signal	2-4,3-5			
Default setting: 3-5, 4-6				
	5 ¹ 6			

Jumper: JP2 (COM 2)

Type: onboard 3 x 2-pin header

Power Mode	JP2
Standard COM Port	3-5,4-6
Pin1 with 5V signal	1-3,4-6
Pin9 with 12V signal	2-4,3-5
Default setting: 3-5, 4-6	1 2 2 5 2 6

2.3 <Connector Reference>

2.3.1 <Internal Connectors>

Connector	Function	Remark
CPU	Socket 478 for socket-P CPU	
SO-DIMM	204-pin DDR3 SO-DIMM slot	
LPT	26-pin LPT port connector	
SATA1/2/3/4	7-pin Serial ATA connector	
DC_IN	4-pin DC 12V input connector	DC input Mode
ATX	20-pin power input connector	ATX input Mode
	(ATX 20-pin power output)	(DC output Mode)
CN_AUDIO	5 x 2-pin audio connector	
CN_PS2	PS/2 keyboard and mouse connector	
CD_IN	4-pin CD-ROM audio input connector	
CN_DIO	6 x 2-pin digital I/O connector	
CN_USB	5 x 2-pin USB connector	
CPUFAN	4-pin CPU cooler fan connector	
SYSFAN	3-pin system cooler fan connector	
CN_DVI	13 x 2-pin DVI interface	(Only 3308550C)
CN_HDTV	5 x 2-pin HDTV interface	
CN_LVDS	20 x 2-pin LVDS connector	
CN_INV	5-pin LCD inverter connector	
CN_IR	5-pin IrDA connector	
JFRNT	14-pin front panel switch/indicator	
	connector	
MiniPCI	Mini-PCI socket Type IIIA	
PCI	32-bit PCI slot	
COM 3/4/5/6	Serial port connector	
CN_CRT2	5 x 2 pin CRT interface	(Only 3308550B)

2.3.2 <External Connectors>

Connector	Function	Remark
CRT	DB15 VGA connector	
RJ45	Six RJ45 LAN connector	
USB	Dual USB 2.0 connector	
COM 1	DB9 Serial port connector	
DC_2P	DC 9~24V input jack	

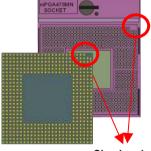
2.4 <CPU and Memory Setup> 2.4.1 <CPU Setup>

The board comes with the socket 478 for Intel Core 2 Duo **socket-P** processor only it supports new generation with 667/800/1066 MHz of front side bus. Please follow the instruction to install the CPU properly.



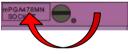
1. Use the flat-type screw drive to unlock the CPU socket



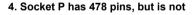


Check point

2. Follow the pin direction to install the processor on the socket

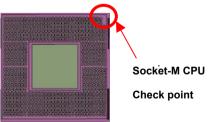


3. Lock the socket



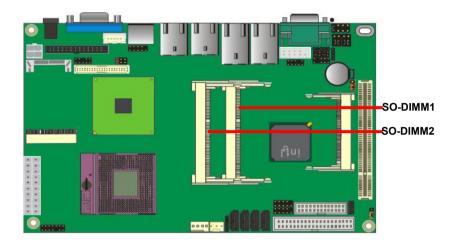
pin-compatible with Socket M

CPU.



2.4.2 <Memory Setup>

The board provides two 204-pin DDR3 SO-DIMM to support 800/1066 MHz memory module up to 4GB. Non-ECC, unbuffered memory is supported only, dual channel technology is enabled automatically for higher performance.



2.5 <CMOS Setup>

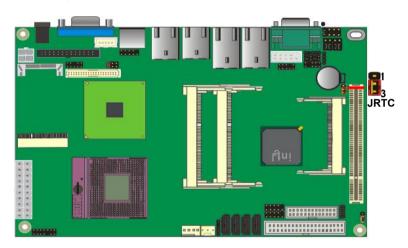
The board's data of CMOS can be setting in BIOS. If the board refuses to boot due to inappropriate CMOS settings, here is how to proceed to clear (reset) the CMOS to its default values.

Jumper: JRTC

Type: Onboard 3-pin jump

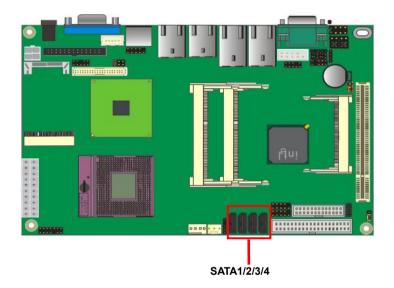
1-2 Clear CMOS	JRTC	Mode
	1-2	Clear CMOS
2-3 Normal Operation	2-3	Normal Operation

Default setting: 2-3



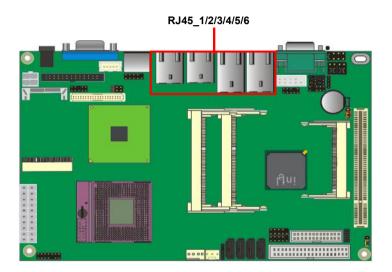
2.7 <Serial ATA Interface>

Based on Intel ICH9M, the board provides four Serial ATAII interfaces with up to 300MB/s of transfer rate.



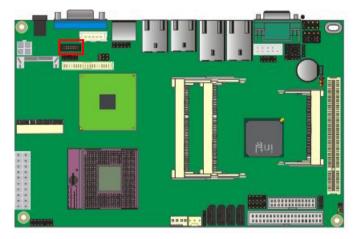
2.8 <Ethernet Interface>

The board integrates with two Intel 82574L Gigabit Ethernet controllers. The Intel Gigabit Ethernet supports triple speed of 10/100/1000Base-T, with IEEE802.3 compliance and Wake-On-LAN supported.



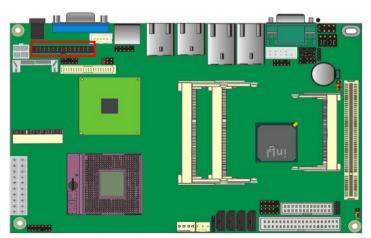
2.9 <Onboard Display Interface>

Based on Intel GM45 chipset with built-in GMA (Graphic Media Accelerator) X4500 graphics, the board provides, 40-pin LVDS interface with 5-pin LCD backlight inverter connector. The board provides dual display function with clone mode and extended desktop mode for VGA, LVDS, HDTV and DVI.



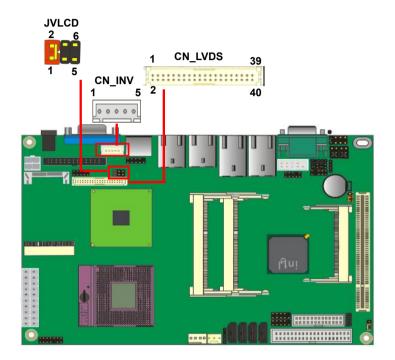
2 x VGA type: (3308550B)

1 x DVI type: (3308550C)



2.10.1 < Digital Display>

The board provides one 40-pin LVDS connector up to two mode for 18/24-bit single/dual channel panels, supports up to 1600 x 1200 (UXGA) resolution, with LCD backlight inverter connector and jumper for panel voltage setting.



Effective patterns of connection: 1-2 / 3-4 / 5-6



Warning: others cause damages

Connector: CN_INV

Type: 5-pin LVDS Power Header

Pin	Description
1	+12V
2	CTLBKL
3	GND
4	GND
5	ENABKL

Connector: **JVLCD** Type: 6-pin Power select Header

Pin	Description	
1-2	LCDVCC (3.3V)	
3-4	LCDVCC (5V)	
5-6	LCDVCC (12V)	

Default setting: 1-2

Connector: CN_LVDS

Type: onboard 40-pin connector for LVDS connector Connector model: **HIROSE DF13-40DP-1.25V**

Pin	Signal	Pin	Signal
2	LCDVCC	1	LCDVCC
4	GND	3	GND
6	ATX0-	5	BTX0-
8	ATX0+	7	BTX0+
10	GND	9	GND
12	ATX1-	11	BTX1-
14	ATX1+	13	BTX1+
16	GND	15	GND
18	ATX2-	17	BTX2-
20	ATX2+	19	BTX2+
22	GND	21	GND
24	ACLK-	23	BTX3-
26	ACLK+	25	BTX3+
28	GND	27	GND .
30	ATX3-	29	BCLK-
32	ATX3+	31	BCLK+
34	GND	33	GND
36	DDCPCLK	35	N/C
38	DDCPDATA	37	N/C
40	N/C	39	N/C

To setup the LCD, you need the component below:

- 1. A panel with LVDS interfaces.
- 2. An inverter for panel's backlight power.
- 3. A LCD cable and an inverter cable.

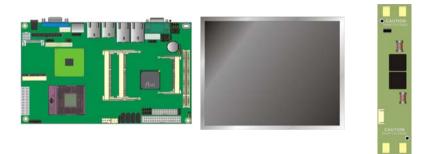
For the cables, please follow the pin assignment of the connector to make a cable,

because every panel has its own pin assignment, so we do not provide a standard cable;

please find a local cable manufacture to make cables.

LCD Installation Guide:

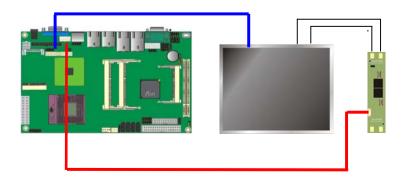
1. Preparing the 3308550, LCD panel and the backlight inverter



- Please check the datasheet of the panel to see the voltage of the panel, and set the jumper JVLCD to +12V or +5V or +3.3V.
- 3. You would need a LVDS type cable.



4. To connect all of the devices well.



After setup the devices well, you need to select the LCD type in the BIOS. The panel type mapping is list below:

	BIOS panel type selection form (BIOS Version:1.0)			
	18-bit Single channel		24-bit Dual channel	
NO.	Output format	NO.	Output format	
1	640 x 480	11	1280 x 768	
2	800 x 480	12	1280 x 1024	
3	800 x 600	13	1600 x 1200	
4	1024 x 768	14	1920 x 1080	
5	1280 x 800	15	1920 x 1200	
	18-bit Dual channel			
6	1280 x 768			
	24-bit Single channel			
7	1024 x 768			
8	1280 x 768			
9	1280 x 800			
10	1366 x 768			

2.10.2 <HDTV Interface>

The board provides an HDTV interface with Intel GM45, supports Composite,

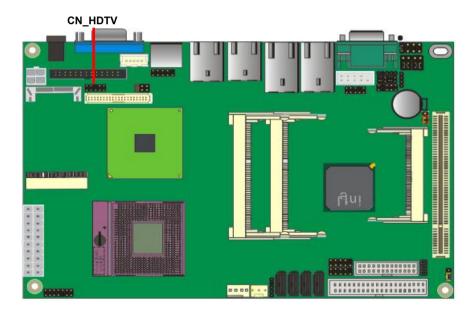
S-Video and Component with PAL and NTSC of TV system, and display (clone or extended desktop) function with VGA,LVDS,DVI.

Connector: CN_HDTV

Connector type: 10-pin header HDTV connector (pitch = 2.54mm)



Pin Number	Assignment	Pin Number	Assignment
1	GND	2	DACB_L
3	DACC_L	4	GND
5	GND	6	N/C
7	DACA_L	8	GND
9	N/C	10	N/C



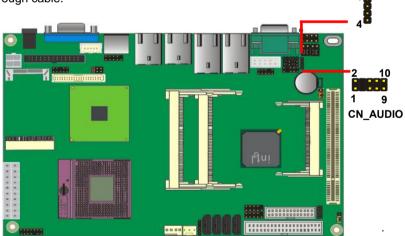
2.11 < Integrated Audio Interface>

The board integrates onboard audio interface with REALTEK ALC888 codec, with Intel next generation of audio standard as High Definition Audio, it offers more sound and other advantages than former HD audio compliance.

The main specifications of ALC888 are:

- High-performance DACs with 97dB SNR (A-Weighting),
- Ten DAC channels support 16/20/24-bit PCM format for 2 sound playback, plus 2 channels of independent stereo sound output (multiple streaming) through the front panel output
- High-quality analog differential CD input
- Meets performance requirements for Microsoft WLP 3.0 Premium desktop and mobile PCs

The board provides 2 channels audio speaker out and Mic-In ports for front I/O panel through cable.



Connector: CN_AUDIO

Type: 10-pin (2×5) 2.54mm-pitch header



Pin	Description	Pin	Description
1	MIC2_L	2	Ground
3	MIC2_R	4	VCC
5	FP_OUT_R	6	MIC2_JD
7	SENSE_B	8	N/C
9	FP_OUT_L	10	LINE2_JD



Connector: CD_IN

Type: 4-pin header (pitch = 2.54mm)

Pin	Description
1	CD – Left
2	Ground
3	Ground
4	CD – Right

2.12 <GPIO Interface>

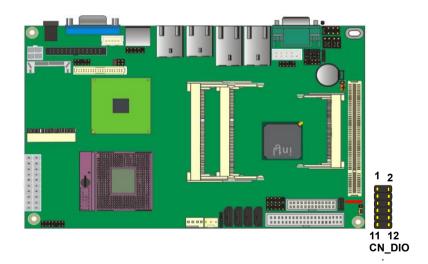
The board provides a programmable 8-bit digital I/O interface; you can use this general purpose I/O port for system control like POS or KIOSK.

Connector: CN_DIO

Type: 12-pin (6 x 2) 2.0mm-pitch header



Pin	Description	Pin	Description
1	Ground	2	Ground
3	GP10	4	GP14
5	GP11	6	GP15
7	GP12	8	GP16
9	GP13	10	GP17
11	VCC	12	+12V



2.13 <Power Supply>

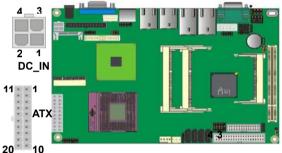
2.13.1.1<DC_IN Input>

The board requires 4-pin DC 12V input or onboard 20-pin ATX2.0, for the input current, please take a reference of the power consumption report on appendix.

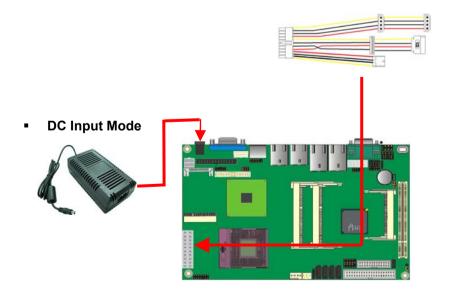
Connector: DC_IN

Type: 4-pin DC power connector

Pin	Description	Pin	Description
1	Ground	2	Ground
3	+12V	4	+12V



ATX power output cable to powering device.

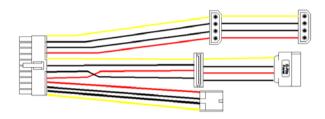


2.13.1.2 <ATX Output>

The board provides one 20-pin ATX connector for +5V/+12V output for powering your HDD, CDROM or other devices when DC-input mode has been used.

Attention: When DC-IN had power supplied, the ATX become output !

Avoid DC-IN and ATX power supply input at the same time !



Connector: **ATX Output** (When DC-IN be used) Type: 20-pin ATX connector for +5V/+12V

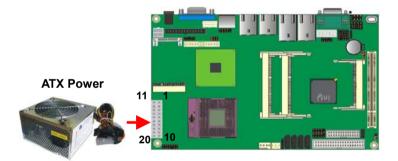
PIN	Assignment	PIN	Assignment
1	*	13	*
2	*	14	*
3	*	15	*
4	5V	16	*
5	GND	17	*
6	*	18	GND
7	GND	19	GND
8	*	20	*
9	*	21	*
10	12V	22	5V

Note: Maximum output voltage: 12V/2A & 5V/3A

2.13.2 <ATX Power Mode>

Connector: **ATX** *(It also can become Output when DC-IN be used)* Type: 20-pin ATX power connector

PIN	Assignment	PIN	Assignment
1	3.3V	11	3.3V
2	3.3V	12	-12V
3	GND	13	GND
4	5V	14	PS_ON
5	GND	15	GND
6	5V	16	GND
7	GND	17	GND
8	PW_OK	18	-5V
9	5V_SB	19	5V
10	12V	20	5V



2.14 <Switch and Indicator>

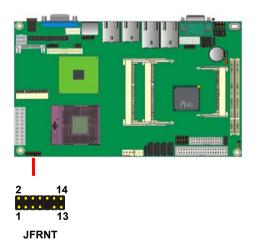
The JFRNT provides front control panel of the board, such as power button, reset

and beeper, etc. Please check well before you connecting the cables on the chassis.

Connector: JFRNT

Type: onboard 14-pin (2 x 7) 2.54-pitch header

Function	Signal	PIN		Signal	Function
IDE LED	HDLED+	1	2	PWRLED+	Power
	HDLED-	3	4	N/C	LED
Reset	Reset+	5	6	PWRLED-	LLD
Reset	Reset-	7	8	SPK+	
	N/C	9	10	N/C	Speaker
Power	PWRBT-	11	12	N/C	Speaker
Button	PWRBT+	13	14	SPK-	



Chapter 3 <System Setup>

3.1 < Audio Configuration>

The board integrates Intel® ICH9M with REALTEK® ALC888 codec. It can support 2 channels sound under system configuration. Please follow the steps below to setup your sound system.

- 1. Install REALTEK HD Audio driver.
- 2. Lunch the control panel and Sound Effect Manager.



3. Select Speaker Configuration



4. Select the sound mode to meet your speaker system.

3.2 <Video Memory Setup>

Based on Intel® GM45 chipset with GMA (Graphic Media Accelerator) X4500, the board supports Intel® DVMT (Dynamic Video Memory Technology) 4.0, which would allow the video memory to be allocated up to 384MB.

To support DVMT, you need to install the Intel GMA X4500 Driver with supported OS.

BIOS Setup:

System BIOS Cacheable	[Enabled]		Ite
Memory Hole At 15M-16M Support FSB and DDR3 667M	h[Disabled]	Menu	Level
► LAN Control VT-d			
	(Disubleu)		
PEG/Onchip VGA Control	[Auto]		
PEG Force X1	[Disabled]	1	
On-Chip Frame Buffer Size	[64MB]		
DVMT Mode	[Enable]		
Total GFX Memory	[128MB]		
PAVP Mode	[PAVP-Lite]		
** UCO Post Doutes Pottin			
** VGA Boot Device Settin			
	[AUTO]		
Panel Scaling	[AUTO]		
LCD Type	[3]		
TV Standard Type	[NTSC_M]		

Total GFX Memory Size:

This item can let you select a static amount of page-locked graphics memory which will be allocated during driver initialization. Once you select the memory amount, it will be no longer available for system memory.

DVMT Mode: This item can let you select graphics memory

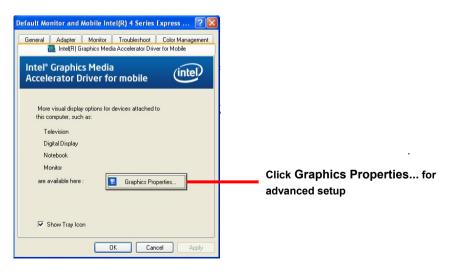
3.3 < Display Properties Setting>

Based on Intel GM45 GMCH with GMA X4500 (Graphic Media Accelerator), the board supports two DACs for display device as different resolution and color bit. Please install the Intel Graphic Driver before you starting setup display devices.

1. Click right button on the desktop to lunch display properties

spicyr	Propertie	5			?
Themes	Desktop	Screen Saver	Appearance Se	ettings	
Drag th	e monitor ic	ons to match the	e physical arrangen	nent of your r	nonitors.
Display 1. Def		on Mobile Intel(I	2	s Chipset Far	nily 文
Scree	n resolution	More	Color quality	1.22	
	800 by 6)0 pixels	Highest (32	DIC	
		as the primary n dows desktop or Identify		Adva	

2. Click Advanced button for more specificity setup.



3. This setup options can let you define each device settings.

Click Monitor to setup the CRT monitor for Colors, Resolution = and Refresh Rate

Click Intel(R) Dual Display Clone to setup the dual display mode as same screen

Intel ^e Graphics Media Accelerator Driver	Monitor and	Notebook	Scheme Option
Display Devices	Single Display	C Notebook	
Display Settings			
Color Correction	Multiple Display	Primary Device	
Unt Knum	 Intel(R) Dual 	Monitor	·
(intel)	Display Clone C Extended Desktop	Secondary Device Notebook]
Launch Zoom	3D Settings		
Information	Video Overlay		

Chapter 4 < BIOS Setup>

The motherboard uses the Award BIOS for the system configuration. The Award BIOS in the single board computer is a customized version of the industrial standard BIOS for IBM PC AT-compatible computers. It supports Intel x86 and compatible CPU architecture based processors and computers. The BIOS provides critical low-level support for the system central processing, memory and I/O sub-systems.

The BIOS setup program of the single board computer let the customers modify the basic configuration setting. The settings are stored in a dedicated battery-backed memory, NVRAM, retains the information when the power is turned off. If the battery runs out of the power, then the settings of BIOS will come back to the default setting.

The BIOS section of the manual is subject to change without notice and is provided here for reference purpose only. The settings and configurations of the BIOS are current at the time of print, and therefore they may not be exactly the same as that displayed on your screen.

To activate CMOS Setup program, press $\langle DEL \rangle$ key immediately after you turn on the system. The following message "Press DEL to enter SETUP" should appear in the lower left hand corner of your screen. When you enter the CMOS Setup Utility, the Main Menu will be displayed as **Figure 4-1**. You can use arrow keys to select your function, press $\langle Enter \rangle$ key to accept the selection and enter the sub-menu.

Esc : Quit F9 : Menu in BIOS F10 : Save & Exit Setup	↑↓→← : Select Item
▶ PC Health Status	Exit Without Saving
▶ PnP/PCI Configurations	Save & Exit Setup
▶ Power Management Setup	Set User Password
▶ Integrated Peripherals	Set Supervisor Password
▶ Advanced Chipset Features	Load Optimized Defaults
► Advanced BIOS Features	Load Fail-Safe Defaults
Standard CMOS Features	Frequency/Voltage Control

Figure 4-1 CMOS Setup Utility Main Screen

Appendix A <I/O Port Pin Assignment>

A.1 <Serial ATA Port>

Connector: SATA1/2/3/4

Type: 7-pin wafer connector



1	2	3	4	5	6	7
GND	SATA_TXP0	SATA_TXN0	GND	SATA_RXN0	SATA_RXP0	GND

A.2 <IrDA Port>

Connector: CN_IR

Type: 5-pin header for SIR Ports

Pin	Description
1	VCC
2	N/C
3	IRRX
4	Ground
5	IRTX



A.3 <SMBUS Port>

Connector: CN_ SMBUS

Type: 5-pin header for SMBUS Ports

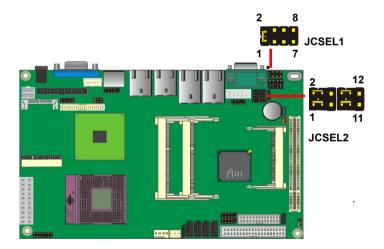
Pin	Description	
1	V5S	
2	N/C	
3	SMBDATA	-
4	SMBCLK	-
5	Ground	_

A.4 <Serial Port 2>

	ctor: CN_COM2 9-pin box header		2 10 1 9
Pin	Description	Pin	Description
1	DCD/422TX-/485-/+5V	2	RX/422TX+/485+
3	TX/422RX+	4	DTR/422RX-
5	Ground	6	DSR
7	RTS	8	CTS
9	RI /+12V		

Setting RS-232, RS-422, RS-485 & IrDA:

Function	JCSEL1	JCSEL2
IrDA		2 12 2 0 0 0 1 11
RS-422		2 12 8 8 8 1 1 11
RS-485		2 12 8 8 8 1 1 11
RS-232		2 12 1 1 1 1



Conn	Parallel nector: LPT ype: 26-Pin		ler			0 0 0 0 0 0 0 0 0 0		26 • • • • 13	
	Pin	Descrip	tion		Pin	Descri	otion		
	1	-PSTB			14	AFD-			
	2	PRO0			15	ERR-			
	3	PRO1			16	INT-			
	4	PRO2			17	SLIN-			
	5	PRO3			18	Ground			
	6	PRO4			19	Ground			
	7	PRO5			20	Ground			
	8	PRO6			21	Ground			
	9	PR07			22	Ground			
	10	ACK-			23	Ground			
	11	BUSY			24	Ground			
	12	PE			25	Ground			
	13	SLCT			26	N/C			
С	Connector: F ype: RJ45 c	RJ45_1/2	· with LE	D		U U		1 8	
	Pin	1	2	3	4	5	6	7	8
C	Description	MI0+	MI0-	MI1+	MI2+	MI2-	MI1-	MI3+	MI3-

A.7 <USB Interface>

Connector: CN_USB 2/3/4

Type: 10-pin (5×2) header for dual USB Ports



Pin	Description	Pin	Description
1	VCC	2	VCC
3	Data0-	4	Data1-
5	Data0+	6	Data1+
7	Ground	8	Ground
9	Ground	10	N/C

A.8 <COM3/4/5/6 Port>

Connector: COM3/4/5/6

Type: 40-Pin box header



Pin	Description	Pin	Description
1	HS_DCD1	2	HS_RXD1
3	HS_TXD1	4	HS_DTR1
5	Ground	6	HS_DSR1
7	HS_RTS1	8	HS_CTS1
9	HS_RI1	10	N/C
11	HS_DCD2	12	HS_RXD2
13	HS_TXD2	14	HS_DTR2
15	Ground	16	HS_DSR2
17	HS_RTS2	18	HS_CTS2
19	HS_RI2	20	N/C
21	HS_DCD3	22	HS_RXD3
23	HS_TXD3	24	HS_DTR3
25	Ground	26	HS_DSR3
27	HS_RTS3	28	HS_CTS3
29	HS_RI3	30	N/C
31	HS_DCD4	32	HS_RXD4
33	HS_TXD4	34	HS_DTR4
35	Ground	36	HS_DSR4
37	HS_RTS4	38	HS_CTS4
39	HS_RI4	40	N/C

A.9 <DVI Port>

2 26 1 25

Connector: CN_DVI

Type: onboard 26-pin connector for DVI connector

Pin	Description	Pin	Description
1	TDC1+	2	TDC1-
3	GND	4	GND
5	TLC+	6	TLC-
7	GND	8	V5S
9	N/C	10	N/C
11	TDC2+	12	TDC2-
13	GND	14	GND
15	TDC0+	16	TDC0-
17	N/C	18	HPD
19	DVI_DA	20	DVI_SL
21	GND	22	BR
23	BG	24	BB
25	5HSYNC	26	5VSYNC

A.10 <CRT Port>



Connector: **CN_CRT2** Type: onboard 10-pin connector for CRT connector

Pin	Description	Pin	Description
1	DATA	2	SL
3	G	4	R
5	GND	6	В
7	HSYNC	8	VSYNC
9	GND	10	GND

Appendix C <System Resources>

C1. <Direct Memory Access (DMA)>

Direct memory access (DMA)

- 2 Standard floppy disk controller
- 4 Direct memory access controller

C2. <Input /Output (IO)>

Input/output (IO)
🔤 🧕 [00000020 - 00000021] Programmable interrupt controller
- 🧕 [00000022 - 0000003F] Motherboard resources
- 🚋 [00000060 - 00000060] Standard 101/102-Key or Microsoft Natural PS/2 Keyboard
🦳 🚽 [00000279 - 00000279] ISAPNP Read Data Port

[000002E8 - 000002EF] Communications Port (COM4)
[000002F8 - 000002FF] Communications Port (COM2)
[00000378 - 0000037F] Printer Port (LPT1)
👰 [00000380 - 00000388] Mobile Intel(R) 4 Series Express Chipset Family
👰 [000003C0 - 000003DF] Mobile Intel(R) 4 Series Express Chipset Family
[000003E8 - 000003EF] Communications Port (COM3)
a [000003F0 - 000003F5] Standard floppy disk controller
[000003F7 - 000003F7] Standard floppy disk controller
[000003F8 - 000003FF] Communications Port (COM1)
🖳 [00000400 - 000004BF] Motherboard resources
夏 [000004D0 - 000004D1] Motherboard resources
[000004E8 - 000004EF] Communications Port (COM6)
[000004F8 - 000004FF] Communications Port (COM5)
😼 [00000500 - 0000051F] Intel(R) ICH9 Family SMBus Controller - 2930
🍠 [00000778 - 0000077B] Printer Port (LPT1)
😼 [00000880 - 0000088F] Motherboard resources
😼 [00000A79 - 00000A79] ISAPNP Read Data Port
🚽 [00000D00 - 0000FFFF] PCI bus
😼 [00007000 - 00007FFF] Intel(R) ICH9 Family PCI Express Root Port 3 - 2944
🕮 [00007F00 - 00007F1F] Intel(R) 82574L Gigabit Network Connection #2
😼 [00008000 - 00008FFF] Intel(R) ICH9 Family PCI Express Root Port 2 - 2942
[00008F00 - 00008F1F] Intel(R) 82574L Gigabit Network Connection
😼 [00009000 - 00009FFF] Intel(R) ICH9 Family PCI Express Root Port 1 - 2940
🕮 [00009F00 - 00009F1F] Intel(R) 82574L Gigabit Network Connection #4
😼 [0000A000 - 0000AFFF] Intel(R) ICH9 Family PCI Express Root Port 6 - 294A
🕮 [0000AF00 - 0000AF1F] Intel(R) 82574L Gigabit Network Connection #3
😼 [0000B000 - 0000BFFF] Intel(R) ICH9 Family PCI Express Root Port 5 - 2948
🕮 [0000BF00 - 0000BF1F] Intel(R) 82574L Gigabit Network Connection #5
💂 [0000C000 - 0000CFFF] Intel(R) ICH9 Family PCI Express Root Port 4 - 2946

🕮 [0000CF00 - 0000CF1F] Intel(R) 82574L Gigabit Network Connection #6
[0000EC00 - 0000EC0F] Intel(R) ICH9M/M-E 2 port Serial ATA Storage Controller 2 - 292C
[0000ED00 - 0000ED0F] Intel(R) ICH9M/M-E 2 port Serial ATA Storage Controller 2 - 292C
[0000EE00 - 0000EE03] Intel(R) ICH9M/M-E 2 port Serial ATA Storage Controller 2 - 292D
[0000EF00 - 0000EF07] Intel(R) ICH9M/M-E 2 port Serial ATA Storage Controller 2 - 292D
[0000F000 - 0000F003] Intel(R) ICH9M/M-E 2 port Serial ATA Storage Controller 2 - 292D
[0000F100 - 0000F107] Intel(R) ICH9M/M-E 2 port Serial ATA Storage Controller 2 - 292D
[0000F300 - 0000F30F] Intel(R) ICH9M/M-E 2 port Serial ATA Storage Controller 1 - 2928
[0000F400 - 0000F40F] Intel(R) ICH9M/M-E 2 port Serial ATA Storage Controller 1 - 2928
[0000F500 - 0000F503] Intel(R) ICH9M/M-E 2 port Serial ATA Storage Controller 1 - 2928
[0000F600 - 0000F607] Intel(R) ICH9M/M-E 2 port Serial ATA Storage Controller 1 - 2928
[0000F700 - 0000F703] Intel(R) ICH9M/M-E 2 port Serial ATA Storage Controller 1 - 2928
[0000F800 - 0000F807] Intel(R) ICH9M/M-E 2 port Serial ATA Storage Controller 1 - 2928
🙀 [0000F900 - 0000F91F] Intel(R) ICH9 Family USB Universal Host Controller - 2936
🙀 [0000FA00 - 0000FA1F] Intel(R) ICH9 Family USB Universal Host Controller - 2935
🙀 [0000FB00 - 0000FB1F] Intel(R) ICH9 Family USB Universal Host Controller - 2934
🙀 [0000FC00 - 0000FC1F] Intel(R) ICH9 Family USB Universal Host Controller - 2939
🙀 [0000FD00 - 0000FD1F] Intel(R) ICH9 Family USB Universal Host Controller - 2938
🙀 [0000FE00 - 0000FE1F] Intel(R) ICH9 Family USB Universal Host Controller - 2937
🙀 [0000FF00 - 0000FF07] Mobile Intel(R) 4 Series Express Chipset Family

C3. <Memory Address Map>

Memory
- 🧕 [000A0000 - 000BFFFF] Mobile Intel(R) 4 Series Express Chipset Family
- 😼 [D0000000 - DFFFFFF] Mobile Intel(R) 4 Series Express Chipset Family
- 😼 [FCC00000 - FCFFFFF] Mobile Intel(R) 4 Series Express Chipset Family
- 🕮 [FD0C0000 - FD0DFFFF] Intel(R) 82574L Gigabit Network Connection #4
[FD0FC000 - FD0FFFFF] Intel(R) 82574L Gigabit Network Connection #4
FD400000 - FD4FFFFF] Intel(R) ICH9 Family PCI Express Root Port 6 - 294A
Intel(R) 82574L Gigabit Network Connection #3
[FD5FC000 - FD5FFFFF] Intel(R) 82574L Gigabit Network Connection #3
[FD600000 - FD6FFFFF] Intel(R) ICH9 Family PCI Express Root Port 5 - 2948
FD700000 - FD7FFFFF] Intel(R) ICH9 Family PCI Express Root Port 5 - 2948
[FD7C0000 - FD7DFFFF] Intel(R) 82574L Gigabit Network Connection #5
[FD7FC000 - FD7FFFFF] Intel(R) 82574L Gigabit Network Connection #5
FD800000 - FD8FFFFF] Intel(R) ICH9 Family PCI Express Root Port 4 - 2946
[FD900000 - FD9FFFFF] Intel(R) ICH9 Family PCI Express Root Port 4 - 2946
FD9C0000 - FD9DFFFF] Intel(R) 82574L Gigabit Network Connection #6

[FD9FC000 - FD9FFFFF] Intel(R) 82574L Gigabit Network Connection #6
📲 🖳 🖓 🚽 🔤 🖓 –
📲 [FDB00000 - FDBFFFFF] Intel(R) ICH9 Family PCI Express Root Port 3 - 2944
[FDBC0000 - FDBDFFFF] Intel(R) 82574L Gigabit Network Connection #2
[FDBFC000 - FDBFFFFF] Intel(R) 82574L Gigabit Network Connection #2
📲 [FDC00000 - FDCFFFFF] Intel(R) ICH9 Family PCI Express Root Port 2 - 2942
📲 🙀 [FDD00000 - FDDFFFFF] Intel(R) ICH9 Family PCI Express Root Port 2 - 2942
[FDDC0000 - FDDDFFFF] Intel(R) 82574L Gigabit Network Connection
🕮 [FDDFC000 - FDDFFFFF] Intel(R) 82574L Gigabit Network Connection
- 🖳 [FDE00000 - FDEFFFFF] Intel(R) ICH9 Family PCI Express Root Port 1 - 2940
- 夏 [FDFF4000 - FDFF7FFF] Microsoft UAA Bus Driver for High Definition Audio
- 👰 [FDFFD000 - FDFFD0FF] Intel(R) ICH9 Family SMBus Controller - 2930
🕰 [FDFFE000 - FDFFE3FF] Intel(R) ICH9 Family USB2 Enhanced Host Controller - 293A
🕰 [FDFFF000 - FDFFF3FF] Intel(R) ICH9 Family USB2 Enhanced Host Controller - 293C
[FEB00000 - FEBFFFFF] Mobile Intel(R) 4 Series Express Chipset Family
- 👰 [FEC00000 - FEC00FFF] System board
- 夏 [FED00000 - FED000FF] System board
- 😨 [FED00000 - FED003FF] High precision event timer
- 😨 [FED13000 - FED1FFFF] System board
- 夏 [FED20000 - FED9FFFF] System board
- 🙀 [FEE00000 - FEE00FFF] System board
- 🙀 [FFB00000 - FFB7FFFF] System board
- 夏 [FFB80000 - FFBFFFFF] Intel(R) 82802 Firmware Hub Device
- 夏 [FFF00000 - FFFFFFF] System board
-

Appendix D < Programming GPIO's>

The GPIO's can be programmed with the MSDOS debug program using simple IN/ OUT commands. The following lines show an example how to do this.

GPIO0GPIO7	bit0bit7	
-o 2E 87		
-o 2E 87	;Enter configuration	
-o 2E 07		
-o 2F 09	;Enable GPIO's function	
-o 2E 30		
-o 2F 02	;Enable GPIO's configuration	
-o 2E F0		
-0 2F xx	;Set GPIO's as input/output; set '1' for	
	input,'0'for output	
-o 2E F1		
-0 2F xx	;If set GPIO's as output, in this register its	
	value can be set	
Optional:		
-o 2E F2		
-0 2F xx	; Data inversion register; '1' inverts the current	
	value of the bits,'0' leaves them as they are	
-o 2E 30		
-o 2F 01	; Active GPIO's	

For further information, please refer to Winbond W83627DHG datasheet.

Appendix E < Programming Watchdog Timer>

The watchdog timer makes the system auto-reset while it stops to work for a period.

The integrated watchdog timer can be setup as system reset mode by program.

Time-out Value Range

- 1 to 255
- Second or Minute

Program Sample

Watchdog timer setup as system reset with 5 second of timeout

2E, 87	
2E, 87	
2E, 07	
2F, 08	Logical Device 8
2E, 30	
2F, 01	Activate
2E, F5	
2F, 00	Set as Second*
2E, F6	
2F, 05	Set as 5

* Minute: bit 3 = 1; Second: bit 3 = 0

You can select Timer setting in the BIOS, after setting the time options, the system will reset according to the period of your selection.

 OnChip IDE Device Super10 Device Hatch Dog Timer Select Onboard Serial Port 3 Serial Port 3 Use IRQ Onboard Serial Port 4 Serial Port 4 Use IRQ Onboard Serial Port 5 Serial Port 5 Use IRQ Onboard Serial Port 6 Serial Port 6 Use IRQ USE Device Setting 	[Press Enter] [Press Enter] [Disabled] [3E8] [IR010] [2E8] [IR011] [4F8] [IR03] [4E8] [IR04] [Press Enter]	Kenu
P DOD DEVICE OF CITINg		

Any advice or comments about our products and service, or anything we can help you with please don't hesitate to contact us. We will do our best to support your products, projects and business.



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